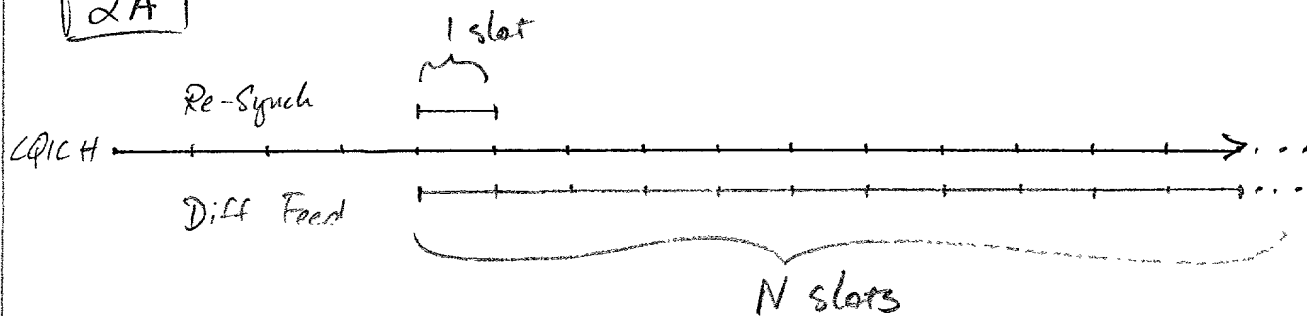
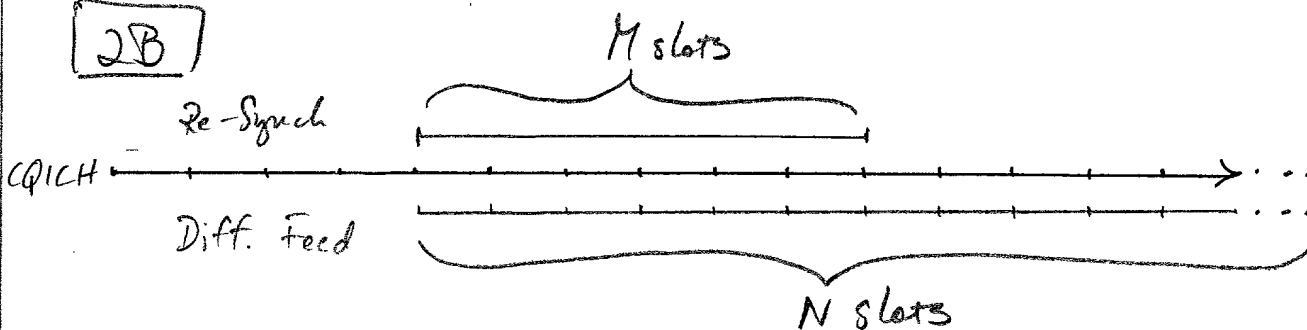


FIG. 1

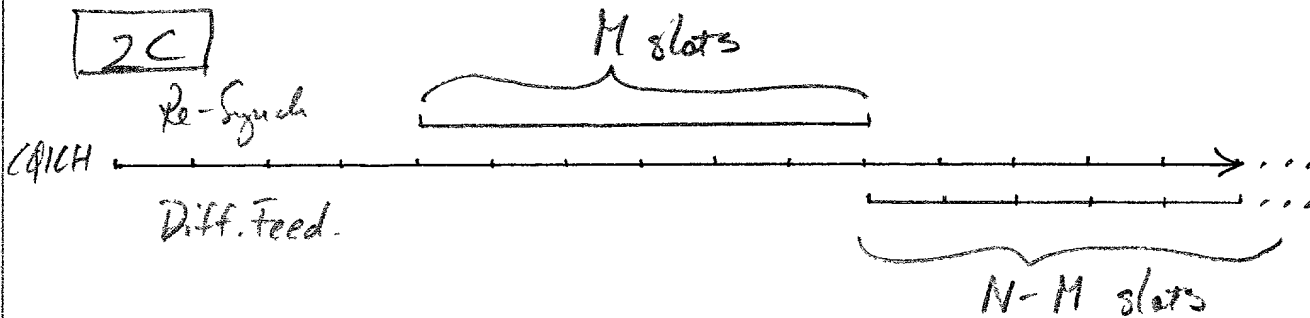
2A



2B



2C





206720 "B.C.C.O.T."

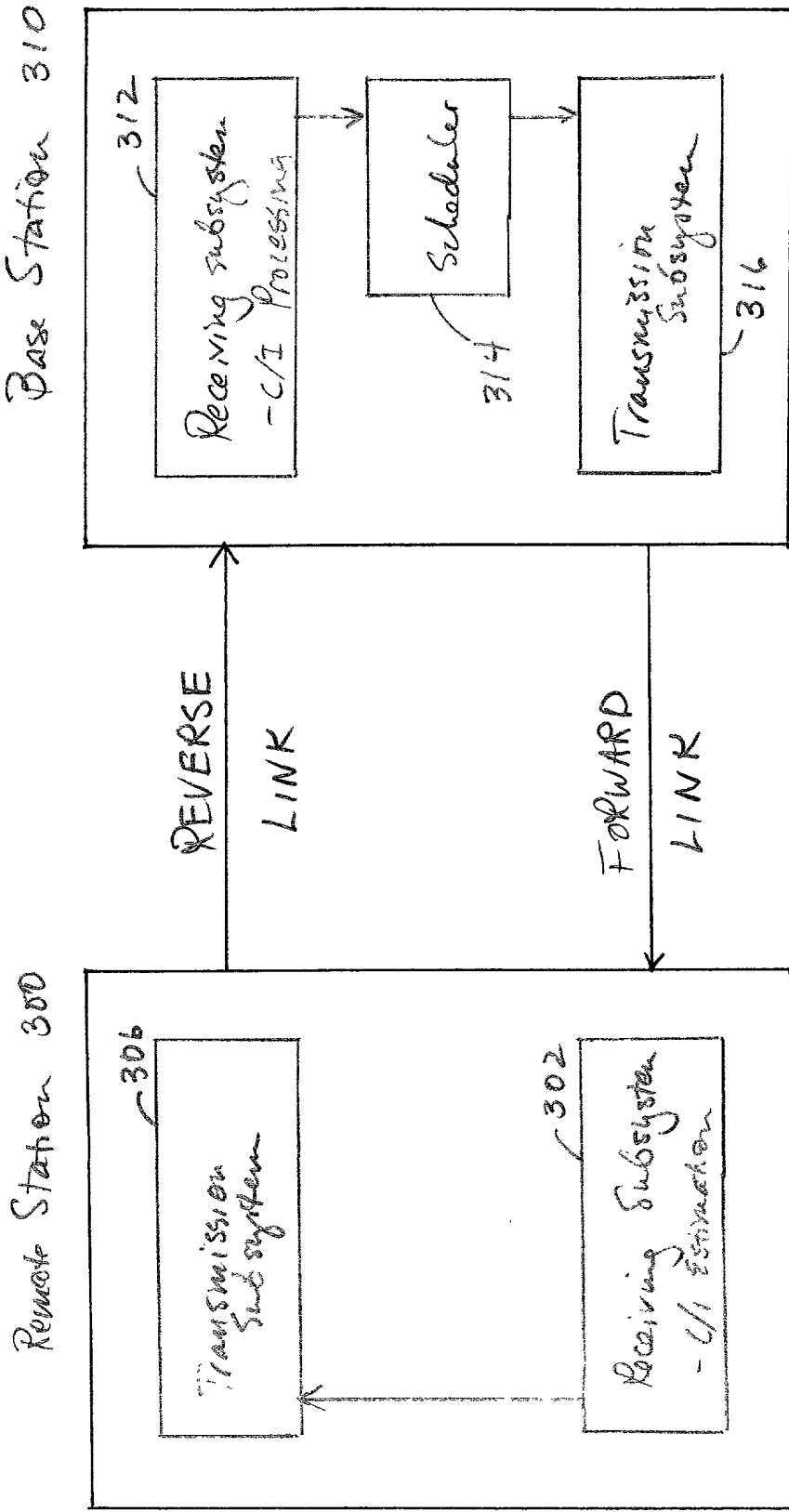
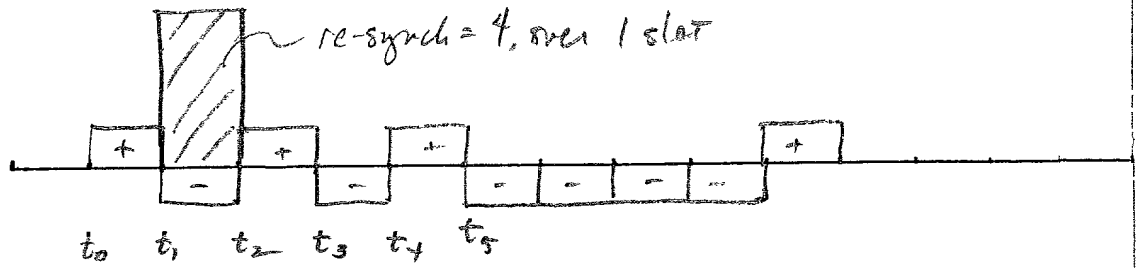
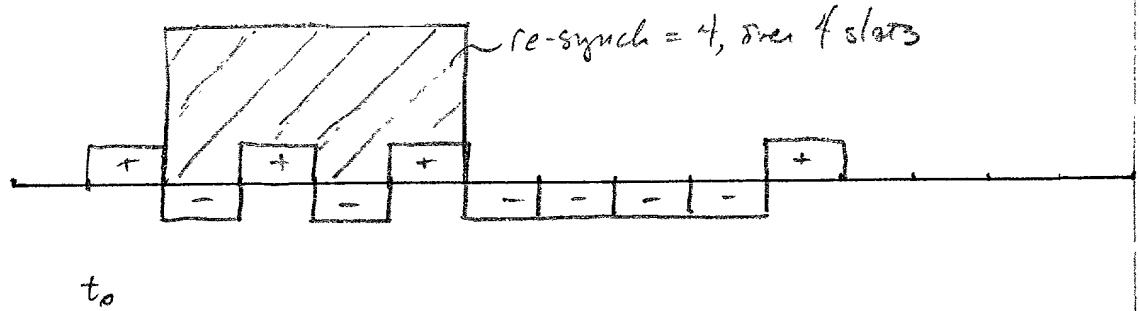


FIG. 3

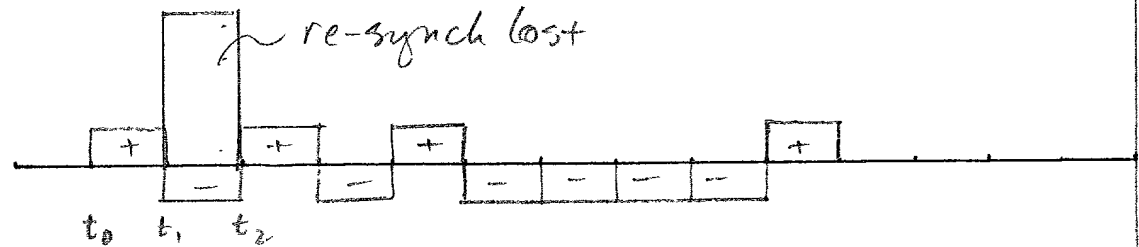
4A



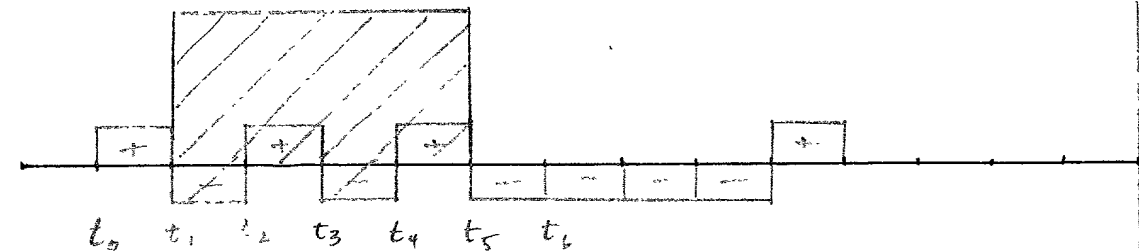
4B



4C



4D



At  $t_0$ , register contents = 10 dB

+ , -  $\Rightarrow$  incremental beats

4A	4B	4C	4D
10	10	10	10
11	11	11	11
4	11	10	10
5	11	11	11
4	11	10	10
5	4	11	5*
4	3	10	4

$t_0 - t_1$

$t_1 - t_2$

$t_2 - t_3$

$t_3 - t_4$

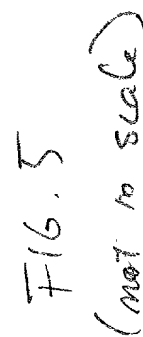
$t_4 - t_5$

$t_5 - t_6$

$t_6 - t_7$

\* Note, that 1st para. is  
dest is discarded in  
accumulation & add

FIG. 4E



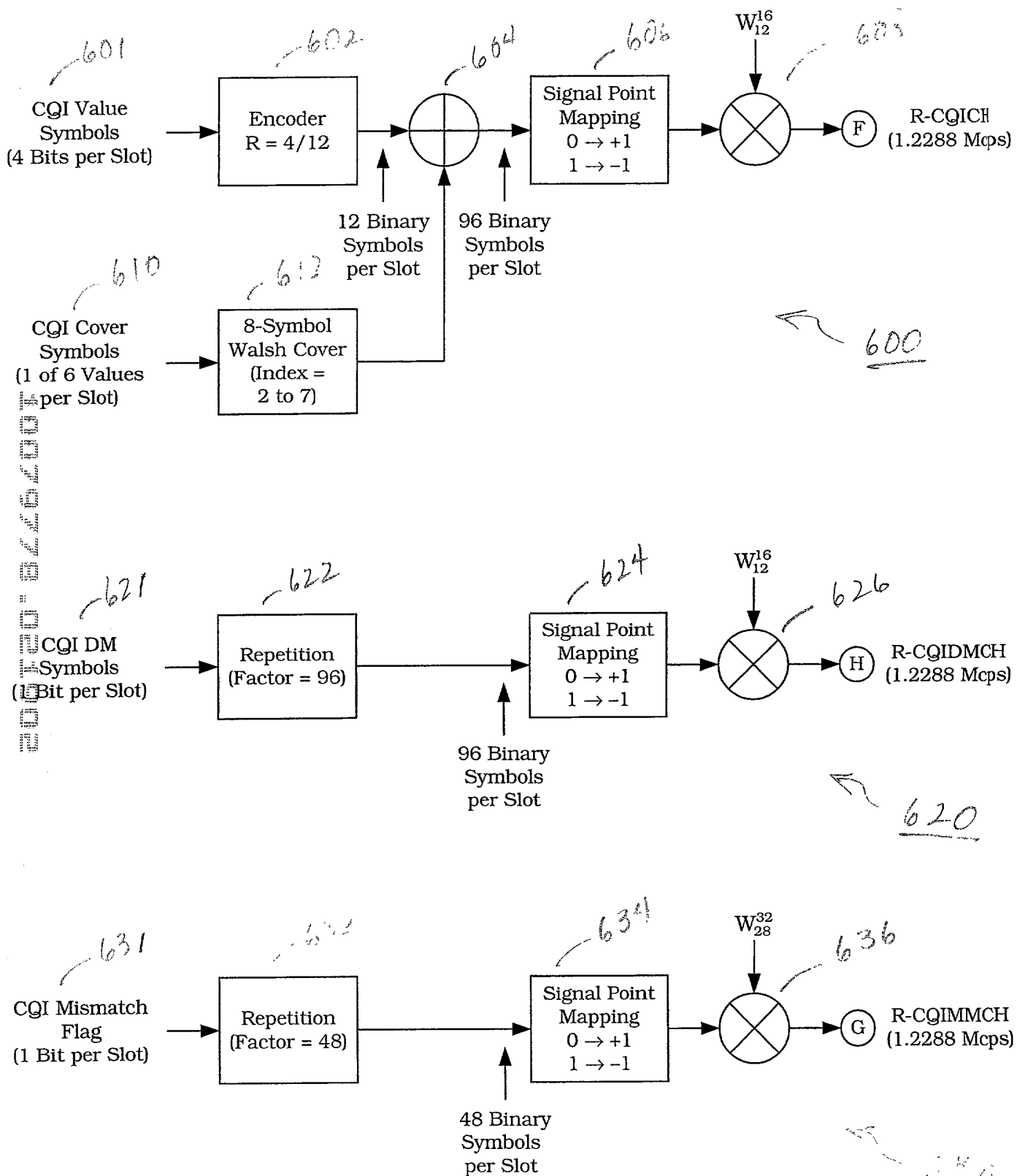


FIG. 6A

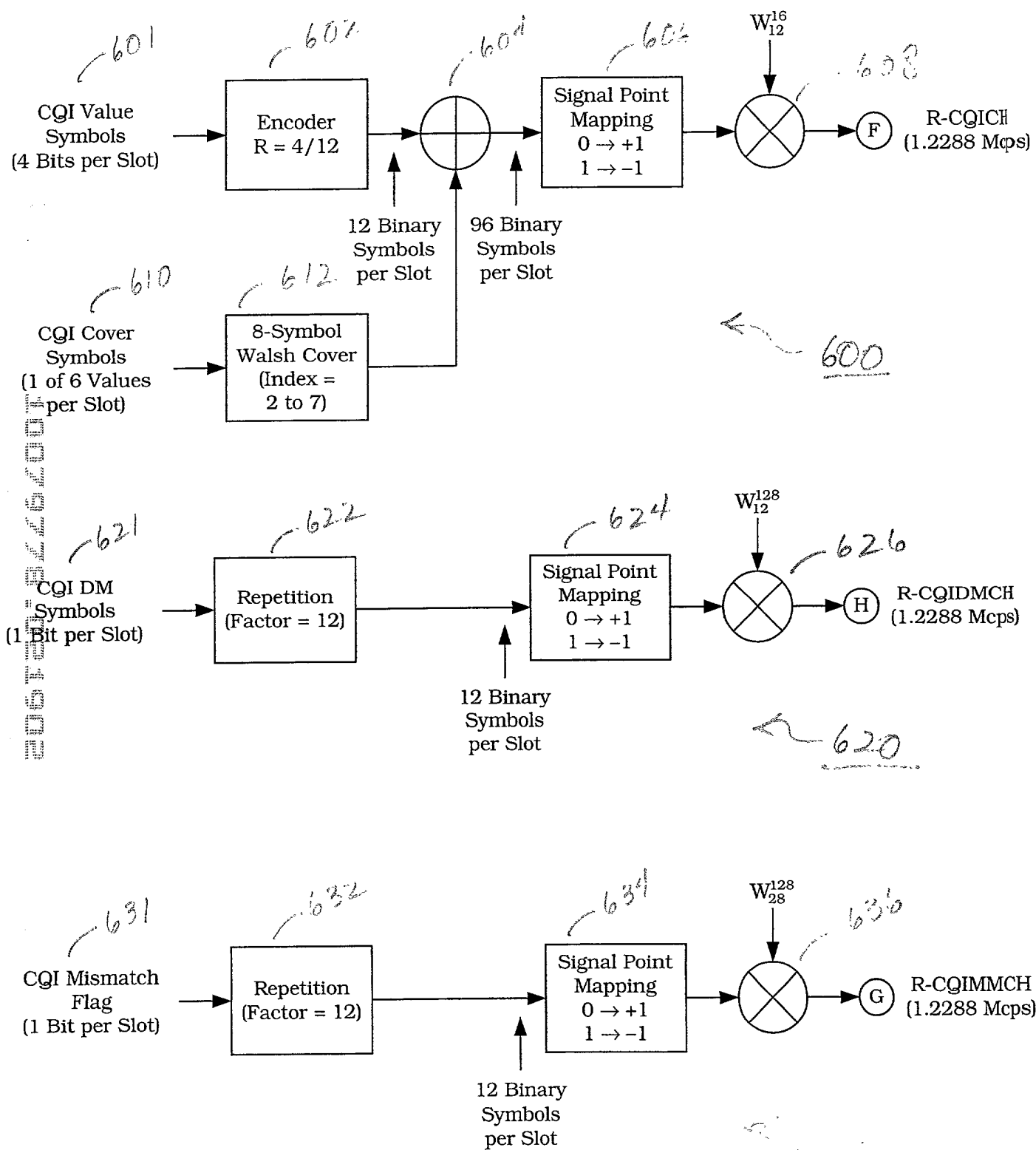
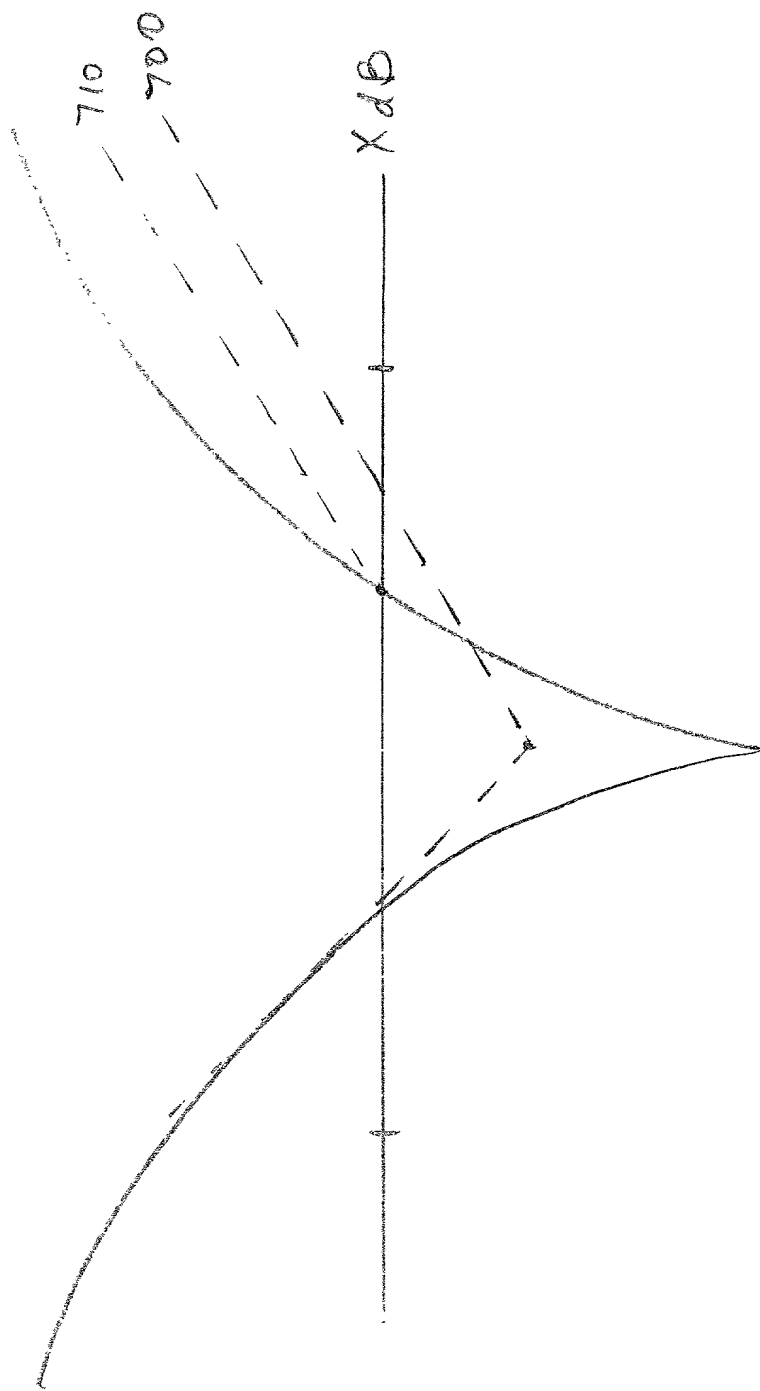


FIG. 6B





206720" 322600"



Re-synch TX over M slots

FIG. 7  
(not to scale)